

FORM PTO-1449 (REV. 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 501265.01	APPLICATION NO. <b>10/748732</b> <del>Not yet assigned</del>
<b>INFORMATION DISCLOSURE STATEMENT</b> <i>(Use several sheets if necessary)</i>		APPLICANT(S) <b>David A. Zimlich</b>	
		FILING DATE <b>12/29/03</b> <del>Concurrently herewith</del>	
		GROUP ART UNIT <del>Not yet assigned</del> <b>2827</b>	

  

U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE <small>IF APPROPRIATE</small>	
AA							
AB							
AC							
AD							
AE							
AF							
AG							
AH							
AI							
AJ							

  

FOREIGN PATENT DOCUMENTS							
DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION		
					YES	NO	
AK							
AL							
AM							
AN							
AO							

  

OTHER PRIOR ART <small>(Including Author, Title, Date, Pertinent Pages, Etc.)</small>	
	AP Favrat, Pierre et al., "A High-Efficiency CMOS Voltage Doubler", IEEE Journal of Solid-State Circuits, Vol. 33, No. 3, March 1998, pp. 410-416.
AQ	

  

EXAMINER	DATE CONSIDERED <b>10/07/2005</b>
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\* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).